

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
BEFORE THE BOARD OF PATENT APPEALS AND INTERFERENCES**

Applicants : Gerald Fagerness et al.
Serial No. : 10/625,954
Filed : July 24, 2003
For : METHODS AND APPARATUS FOR INDEXING MEMORY OF
A NETWORK PROCESSOR
Examiner : Robert C. Scheibel
Group Art Unit : 2619

Mail Stop Reply Brief-Patents
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

REPLY BRIEF

Dear Sir:

In response to the Examiner's Answer mailed June 10, 2008, this Reply Brief is respectfully submitted. For the reasons more fully set forth below, it is maintained that the remaining final rejections of claims 1-19 should be reversed.

OK TO ENTER: /RCS/